

UTILITY PATENT APPLICATION
UNDER 37 CFR 1.53(b)

JC921 U.S. PTO
09/712246
11/15/00

Box PATENT APPLICATION
Assistant Commissioner for Patents
Washington, DC 20231

Case Docket No. 49657-875

Sir:

Transmitted herewith for filing is the patent application of:

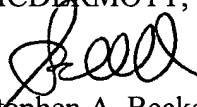
INVENTOR: Tetsushi TANIZAKI, Kazushi SUGIURA, Masami NAKAJIMA
FOR: SEMICONDUCTOR MEMORY DEVICE WITH BUILT-IN SELF TEST
CIRCUIT OPERATING AT HIGH RATE

Enclosed are:

- ☒ 31 pages of specification, claims, abstract.
- ☐ Declaration and Power of Attorney.
- ☒ Priority Claimed.
- ☐ Certified copy of _____
- ☒ 17 sheets of formal drawing.
- ☐ An assignment of the invention to _____
and the assignment recordation fee.
- ☐ An associate power of attorney.
- ☐ Information Disclosure Statement, Form PTO-1449 and reference.
- ☒ Return Receipt Postcard

Respectfully submitted,

MCDERMOTT, WILL & EMERY


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20277
PATENT TRADEMARK OFFICE

Docket No.: 49657-875

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Tetsushi TANIZAKI, et al.

Serial No.:

Group Art Unit:

Filed: November 15, 2000

Examiner:

For: SEMICONDUCTOR MEMORY DEVICE WITH BUILT-IN SELF TEST CIRCUIT
OPERATING AT HIGH RATE



CLAIM OF PRIORITY

Assistant Commissioner for Patents
Washington, DC 20231

Sir:

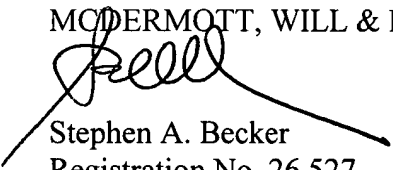
In accordance with the provisions of 35 U.S.C. 119, Applicants hereby claim the priority of:

Japanese Patent Application No. 11-329822,
filed November 19, 1999

A certified copy will be filed in due course.

Respectfully submitted,

MODERMOTT, WILL & EMERY


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